

|  |  |                                     |   |
|--|--|-------------------------------------|---|
| <b>FORM PTO-1449</b><br><br><b>U.S. DEPARTMENT OF COMMERCE</b><br><b>PATENT AND TRADEMARK OFFICE</b><br><br><b>LIST OF REFERENCES CITED BY APPLICANT</b><br><br><i>(Use several sheets if necessary)</i> | <b>ATTORNEY DOCKET NO.</b><br>1075.1174  | <b>APPLICATION NO.</b><br>163964991 | <b>FIRST NAMED INVENTOR</b><br>Akio MATSUDA, et al. |
|  | <b>FILING DATE</b><br>September 28, 2001 | <b>GROUP ART UNIT</b>               | 1c997 U.S. PTO<br>09/28/01<br>09/28/01              |

### U.S. PATENT DOCUMENTS

| EXAMINER<br>INITIAL |    | DOCUMENT<br>NO. | DATE | NAME | CLASS | SUB-<br>CLASS | FILING<br>DATE |
|---------------------|----|-----------------|------|------|-------|---------------|----------------|
|                     | AA |                 |      |      |       |               |                |
|                     | AB |                 |      |      |       |               |                |
|                     | AC |                 |      |      |       |               |                |
|                     | AD |                 |      |      |       |               |                |
|                     | AE |                 |      |      |       |               |                |
|                     | AF |                 |      |      |       |               |                |

### FOREIGN PATENT DOCUMENTS

|  |    | DOCUMENT<br>NO. | DATE | COUNTRY | CLASS | SUB-<br>CLASS | TRANSLATION<br>YES NO |  |
|--|----|-----------------|------|---------|-------|---------------|-----------------------|--|
|  | AG |                 |      |         |       |               |                       |  |
|  | AH |                 |      |         |       |               |                       |  |
|  | AI |                 |      |         |       |               |                       |  |
|  | AJ |                 |      |         |       |               |                       |  |
|  | AK |                 |      |         |       |               |                       |  |
|  | AL |                 |      |         |       |               |                       |  |

### OTHER REFERENCES (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

|            |      |   |
|------------|------|---|
| —          | AM   | K. V. Rompaey, et al., "COWARE – A DESIGN ENVIRONMENT FOR HETEROGENEOUS HARDWARE/SOFTWARE SYSTEMS", (Proceedings EuroDAC), 1996                                       |
| <u>KMN</u> | AN ✓ | S. Vernalde et al., "An Object Oriented Programming Approach for Hardware Design ", (IEEE Computer Society Workshop on VLSI) 4/1999.                                  |
| <u>KMN</u> | AO ✓ | R. Cmar, et al., "A Methodology and Design Environment For DSP Asic Fixed Point Refinement", (Proceedings Design and Test in Europe Conference, pp. 271-276), 3/1999. |

|  |                               |
|--|-------------------------------|
| <b>EXAMINER</b><br>  | <b>DATE CONSIDERED</b> 2/4/05 |
| EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. |                               |

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

ATTORNEY DOCKET NO.

1075.1174

APPLICATION NO.

U.S. PTO  
09/964591

## LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

FIRST NAMED INVENTOR

Akio MATSUDA, et al.

FILING DATE

September 28, 2001

GROUP ART UNIT

## U.S. PATENT DOCUMENTS

| EXAMINER<br>INITIAL |    | DOCUMENT<br>NO. | DATE | NAME | CLASS | SUB-<br>CLASS | FILING<br>DATE |
|---------------------|----|-----------------|------|------|-------|---------------|----------------|
|                     | AA |                 |      |      |       |               |                |
|                     | AB |                 |      |      |       |               |                |
|                     | AC |                 |      |      |       |               |                |
|                     | AD |                 |      |      |       |               |                |
|                     | AE |                 |      |      |       |               |                |
|                     | AF |                 |      |      |       |               |                |

## FOREIGN PATENT DOCUMENTS

|  |    | DOCUMENT<br>NO. | DATE | COUNTRY | CLASS | SUB-<br>CLASS | TRANSLATION<br>YES NO |  |
|--|----|-----------------|------|---------|-------|---------------|-----------------------|--|
|  | AG |                 |      |         |       |               |                       |  |
|  | AH |                 |      |         |       |               |                       |  |
|  | AI |                 |      |         |       |               |                       |  |
|  | AJ |                 |      |         |       |               |                       |  |
|  | AK |                 |      |         |       |               |                       |  |
|  | AL |                 |      |         |       |               |                       |  |

## OTHER REFERENCES (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

|            |      |  |
|------------|------|--|
| <u>KAN</u> | AM ✓ | P. Schaumont, et al., "Hardware Reuse at the Behavioral Level", (The Proceedings of the 36 <sup>th</sup> Design Automation Conference, ACM, 1999, pp. 784-789) June 1999.  |
| <u>KAN</u> | AN ✓ | S. Liao, et al., "An Efficient Implementation of Reactivity for Modeling Hardware in the Scenic Design Environment" Proceedings of the Design Automation Conference DAC' 97, pp. 70-75, June 1999.                     |
| <u>KAN</u> | AO   | A. Gerstlauer, et al., "SpecC SYSTEM – Level Design Methodology Applied to the Design of A GSM Vocoder", pp. 19-26, Proceedings of the Workshop on Synthesis and System Integration of Mixed Technologies) April 1999. |

EXAMINER

K. Halsey

DATE CONSIDERED

2/4/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

ATTORNEY DOCKET NO.

1075.1174

APPLICATION NO.

U.S. PTO  
09/064591

09/28/01

## LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

FIRST NAMED INVENTOR

Akio MATSUDA, et al.

FILING DATE

September 28, 2001

GROUP ART UNIT

## U.S. PATENT DOCUMENTS

| EXAMINER<br>INITIAL |    | DOCUMENT<br>NO. | DATE | NAME | CLASS | SUB-<br>CLASS | FILING<br>DATE |
|---------------------|----|-----------------|------|------|-------|---------------|----------------|
|                     | AA |                 |      |      |       |               |                |
|                     | AB |                 |      |      |       |               |                |
|                     | AC |                 |      |      |       |               |                |
|                     | AD |                 |      |      |       |               |                |
|                     | AE |                 |      |      |       |               |                |
|                     | AF |                 |      |      |       |               |                |

## FOREIGN PATENT DOCUMENTS

|  |    | DOCUMENT<br>NO. | DATE | COUNTRY | CLASS | SUB-<br>CLASS | TRANSLATION<br>YES NO |  |
|--|----|-----------------|------|---------|-------|---------------|-----------------------|--|
|  | AG |                 |      |         |       |               |                       |  |
|  | AH |                 |      |         |       |               |                       |  |
|  | AI |                 |      |         |       |               |                       |  |
|  | AJ |                 |      |         |       |               |                       |  |
|  | AK |                 |      |         |       |               |                       |  |
|  | AL |                 |      |         |       |               |                       |  |

## OTHER REFERENCES (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

|            |    |   |  |
|------------|----|---|--|
| <i>K/S</i> | AM | ✓ | D. Verkest et al., "On the use of C++ For System on Chip Design" (Proceedings of the IEEE Workshop on VLSI (IWV), pp. 42-47, April 1999. |
| —          | AN |   | A. Fukuda, "Parallel Operating System", pp. 30-32, Issued by Corona Publishing Co., Ltd., 1997   |
| —          | AO |   | S. Oaks, et al., "Java Thread Programming, pp. 1-6, Issued by Ohmsha, Ltd., 1997.  |

EXAMINER

*K/S*

DATE CONSIDERED

2/4/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.